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IDS  
6/27/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Date: April 17, 2000

K.K.Chan et al.

Group Art Unit:

Serial No.

Examiner:

Filed: (Herewith)

Docket No. YOR9-1996-0118US3

JC675 U.S. PTO  
09/550990  
04/17/00

For: A SCALABLE MOS FIELD EFFECT TRANSISTOR

Assistant Commissioner for Patents and Trademarks  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97(b)(1)**


Sir:

Under my duty to disclose information under Rule 37 C.F.R. §1.56, enclosed is a copy of an IDS filed June 30, 1998 and February 16, 2000 in Serial No. 09/107,738 filed June 30, 1998 including copies (2 pages) of PTO-1449 form.

Under 37 CFR §1.98(d), copies of the references listed in an IDS are not required to be provided if they were previously cited by or submitted to the Patent Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120.

Please charge any fee necessary to enter this paper to deposit account 09-0468.

Respectfully submitted,

  
Robert M. Trepp  
Reg. No. 25,933  
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IBM CORPORATION  
Intellectual Property Law Dept.  
P.O. Box 218  
Yorktown Heights, New York 10598

<b>FORM PTO-1449 (Modified)</b>	<b>ATTY. DOCKET NO.</b> YO996-118X	<b>SERIAL NO.</b> 09/107,738
<b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>	<b>APPLICANT:</b> Kevin K. Chan et al.	
(Use several sheets if necessary)	<b>FILING DATE:</b> June 30, 1998	<b>GROUP:</b> 2814

JCS75 U.S. PTO  
 09/50990  
 04/17/00

**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							
	AM							
	AN							
	AO							
	AP							

**OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)**

AQ	Handbook of Thin Film Technology, by L.I. Maissel and R. Glang, McGraw-Hill (1970), pages title, 5-1 to 5-3, 5-16 to 5-21, 19-1 to 19-5 and 19-16 to 19-25.
AR	
AS	
AT	
AU	

<b>EXAMINER</b>	<b>DATE CONSIDERED</b>
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**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)				ATTY. DOCKET NO. YO996-118X		SERIAL NO.			
				APPLICANT: Kevin K. Chan et al.					
				FILING DATE: HEREWITH				GROUP:	
U.S. PATENT DOCUMENT									
Examiner Initial	Document Number	Date	Name	Class	Sub-Class	Filing Date			
AA	5,405,787	04/95	Kazumi Kurimoto	HO1L	21/265	10/14/93			
AB	5,272,100	12/93	Satoh et al.	HO1L	21/335	08/07/91			
AC	5,089,863	02/92	Satoh et al.	HO1L	29/10	09/08/88			
AD	5,464,782	11/95	Koh et al.	HO1L	21/265	07/05/94			
AE	5,268,330	12/93	Givens et al.	HO1L	21/283	12/11/92			
AF	5,196,357	03/93	Boardman et al.	HO1L	21/70	11/18/91			
AG	5,231,038	07/93	Yamaguchi et al.	HO1L	21/265	05/27/92			
AH									
AI									
AJ									
AK									
FOREIGN PATENT DOCUMENTS									
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION Y/N			
AL	JP 06-350085	12/94	Japan	HO1L	29/784	Abstract			
AM	JP 04-180633	06/92	Japan	HO1L	21/336	Abstract			
AN	TW 266313	12/95	Taiwan			Abstract			
AO	TW 205579	07/95	Taiwan			Abstract			
AP									
OTHER REFERENCES (Including author, title, date, pertinent pages, etc.)									
AR	A. Acovic et al., "Novel Gate Process for 0.1 Micron MOSFETs", IBM Technical Disclosure Bulletin, Vol. 36, No. 11, November 1993.								
AS									
AT									
EXAMINER				DATE CONSIDERED					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									